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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.	Serial No.	
				M-9327 US	09/658,707	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				Applicants Earl Schreyer et al.		
				Filing Date	Group	
				September 8, 2000	2671	
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*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
/S.H./	AA 5,039,954	Aug. 13, 1991	Bult et al.	330	277	
	AB 5,285,171	Feb. 8, 1994	Bult et al.	330	277	
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Foreign Patent Documents						
						Translation
	Document	Date	Country	Class	Subclass	Yes
	AK					No
	AL					
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
/S.H./	AM	"FMS9884A Graphics Digitizer 3x8-Bit, 100/140 Ms/s Triple Video A/D Converter with Clamps" Fairchild Semiconductor (2000), 30 pages.				
/S.H./	AN	"Computer RGB colour space", downloaded 08/08/2000 from < http://www.robo.mein.nagoya-u.ac.jp/~niumi/color-space/COL_21.htm >				
/S.H./	AO	"Technotes Color Monitor Connections", downloaded 08/08/2000 from < http://devworld.apple.com/technotes/hw/hw_08.html >, Apple Computer, Inc. (2000).				
/S.H./	AP	"The PC Guide", downloaded 08/08/2000 from < http://www.pcguide.com/ref/crt >, The PC Guide (1997-2000), 19 pages.				
Examiner	/Steven Holton/		Date Considered	08/17/2008		
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.						

Notice of References Cited		Application/Control No. 09/658,707	Applicant(s)/Patent Under Reexamination SCHREYER ET AL.	
		Examiner Leland R. Jorgensen	Art Unit 2675	Page 1 of 1

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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.	Application No.			
				24317/81001	09/658,707			
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(Use several sheets if necessary)				Earl Schreyer et al.				
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		Document	Date	Country	Class	Subclass	Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
Examiner Signature: /Steven Holton/			Date Considered: 08/17/2008					
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								

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G	US-			
H	US-			
I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

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/S/H/V	Michael Flynn et al., CMOS Folding A/D Converters with Current-Mode Interpolation, IEEE Journal of Solid-State Circuits, Vol. 31, No. 9, September 1996, pp. 1248 - 1257.
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